
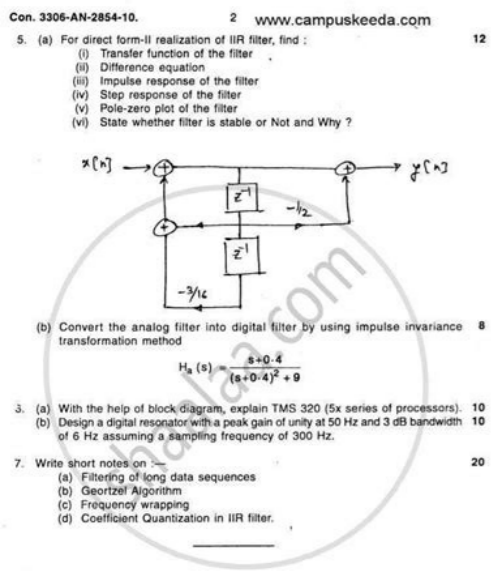


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**Open**

- Consider the following statements  $S_1$  and  $S_2$  :  
 $S_1$  : The  $\beta$  of a bipolar transistor reduces if the base width is increased.  
 $S_2$  : The  $\beta$  of a bipolar transistor increases if the doping concentration in the base is increased.  
 Which one of the following is correct ?  
 (A)  $S_1$  is false and  $S_2$  is true. (B) Both  $S_1$  and  $S_2$  are true.  
 (C) Both  $S_1$  and  $S_2$  are false. (D)  $S_1$  is true and  $S_2$  is false.
- Cut-in voltage of a silicon diode is  
 (A) 0.1 V (B) 0.2 V (C) 0.6 V (D) 0.25 V
- The h-parameter model is more commonly used because  
 (A) h-parameter can be measured more easily  
 (B) h-parameter does not vary with frequency  
 (C) the analysis, with h-parameter gives the same expression for the performance for all configurations  
 (D) h-parameter model is an accurate representation
- Bipolar transistor satisfies the relation  
 (A)  $\beta = \alpha$  (B)  $\beta = \frac{\alpha}{1-\alpha}$  (C)  $\beta = \frac{1}{\alpha}$  (D) none of these
- Consider the following four common type of transistors :  
 1. Point contact transistor 2. Bipolar junction transistor  
 3. MOS field effect transistor 4. Junction field effect transistor  
 The correct arrangement of these transistors in the increasing order of input impedance is



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### TYPICAL QUESTIONS & ANSWERS

#### PART - I

#### OBJECTIVE TYPE QUESTIONS

Each Question carries 2 marks.

Choose correct or the best alternative in the following:

- Q.1** The "Superposition theorem" is essentially based on the concept of  
 (A) duality. (B) linearity.  
 (C) reciprocity. (D) non-linearity.

Ans: B

- Q.2** Cells are connected in parallel in order to  
 (A) increase the voltage available. (B) reduce cost of wiring.  
 (C) increase the current available. (D) reduce the time required to fully charge them after use.

Ans: C

- Q.3** The power factor of a purely resistive circuit is  
 (A) zero. (B) unity.  
 (C) lagging. (D) leading.

Ans: B

- Q.4** The power taken by a 3-phase load is given by the expression  
 (A)  $3 V_L I_L \cos \phi$ . (B)  $\sqrt{3} V_L I_L \cos \phi$ .  
 (C)  $3 V_L I_L \sin \phi$ . (D)  $\sqrt{3} V_L I_L \sin \phi$ .

Ans: B

- Q.5** Which of the following generating stations has the minimum running cost?  
 (A) hydro-electric station. (B) nuclear power station.  
 (C) thermal power station. (D) diesel power plant.

Ans: A

- Q.6** Which of the following motors has a high starting torque?  
 (A) ac series motor. (B) dc series motor.  
 (C) induction motor. (D) synchronous motor.

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#### 1. Define amplitude Modulation.

Amplitude Modulation is the process of changing the amplitude of a relatively high frequency carrier signal in proportion with the instantaneous value of the modulating signal.

#### 2. Define Modulation index and percent modulation for an AM wave.

Modulation index is a term used to describe the amount of amplitude change present in an AM waveform. It is also called as coefficient of modulation. Mathematically modulation index is  $m = E_m / E_c$

Where,  $m$  = Modulation coefficient

$E_m$  = Peak change in the amplitude of the output waveform voltage.

$E_c$  = Peak amplitude of the unmodulated carrier voltage.

Percent modulation gives the percentage change in the amplitude of the output wave when the carrier is acted on by a modulating signal.

#### 3. Define Low level Modulation.

In low level modulation, modulation takes place prior to the output element of the final stage of the transmitter. For low level AM modulator class A amplifier is used.

#### 4. Define High level Modulation.

In high level modulators, the modulation takes place in the final element of the final stage where the carrier signal is at its maximum amplitude. For high level modulator class C amplifier is used.

#### 5. What is the advantage of low level modulation?

An advantage of low level modulation is that less modulating signal power is required to achieve a high percentage of modulation.

#### 6. Distinguish between low level and high level modulation.

In low level modulation, modulation takes place prior to the output element of the final stage of

2. These electrical and communication engineering devices cover conceptual, theoretical and practical. Because practice is the surest way to success. Practice daily to understand and learn and master various topics of electrical and communication engineering. Questions and Answers on Electrical and Communication Engineering Objectives: For the interview, anyone can practice these questions from Electrical and Communication Engineering Objectives and answer them regularly to prepare for the interviews on the subjects of electrical and communication engineering. 3. Electrical and Communication Engineering Multiple Choice Questions: MCQS These Electrical and Communication Engineering Questions and Answers are in MCQS format and focus on the core area of Electrical and Communication Engineering. 3n, which covers basic subjects in the engineering of electronics and communication. By practicing the objective questions of electrical engineering and communication and answering an individual for competitive exams, entrance exams and interviews. Highlights 4 Multiple choice objective questions and answers in electrical engineering and communication with option to discuss each question. One should not worry about downloading incomplete e-books or PDF files in electronics and communication engineering from other places. Just check our page and start practicing. 5 Bank of questions of type of electrical engineering and conceptual communication engineering. Applicant students and working professionals can learn and practice our objective questions of electrical and communication engineering. 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